ALVEO[™] V70 AI INFERENCE ACCELERATOR CARD

OVERVIEW

ALVEO

With intelligent connected devices in our homes, cars, offices, factories, cities, and in the cloud, the cost for this proliferation of Al enabled applications is an exponential increase in the data-processing and energy efficiency requirements placed on the chips powering these devices. The challenge is not just how to deploy the Al model, but how to deploy the Al application most efficiently. The best implementation of an Al application doesn't need to be the fastest, it needs to be the most efficient, yet remain flexible. AMD XDNA architecture built on adaptive computing platforms provide the best of both worlds for Al inference workloads – from cloud, edge or endpoint.

The Alveo[™] V70 accelerator card is the first AMD Alveo production card leveraging AMD XDNA[™] architecture with AI Engines providing a tightly integrated heterogeneous compute platform for CNN, RNN, and NLP acceleration targeting cloud and edge applications. V70 is designed to be the most energy efficient AI Inference card in the AMD portfolio tuned for video analytics and natural language processing workloads and offers industry standard framework support, directly compiling models trained in TensorFlow and PyTorch. The card is a PCle[®]-based half height, half length, single slot card that supports passive cooling for closed-loop thermal control in the server PCle expansion slot. The card is equipped with a 7nm Versal[®] ACAP device which has an integrated AI Engine core to complement adaptable and scalar engines and 16 GB of DDR4 memory. Providing low power and a low-profile form factor, the V70 helps reduce cost per AI channel and provides high channel density for video applications.



TARGET APPLICATIONS

KEY VALUE PROPS

- Helps reduced cost per AI channel
- Entire pipeline execution on accelerator
- High channel density for video analytics applications

TARGET USERS

- Data Scientist
- Al Developers
- Software Developers
- Data Center Infrastructure Engineers

USE CASES

- Video Content Analysis
- Natural Language Processing
- Smart City/ Smart Retail
- Recommendation Ranking
- Medical Imaging

SPECIFICATIONS

| FEATURE | DETAILS |
|------------------------------|--|
| Application | Al Inference |
| Architecture | AMD XDNA – Versal Al Core |
| AI Engine | 2nd-gen AIE-ML tiles |
| TOPS* (INT8) | • 404 |
| TOPS* (BF16) | • 202 |
| Memory Bandwidth | • 47600 GB/s for internal memory • 76.8 GB/s for external DDR |
| High Density Video Decoder** | 96 channels of 1920x1080p H.264/H.265 |
| PCIe interface | • Gen 4/5 x 8 |
| Form Factor | • Half Height, Half Length |
| Cooling | Passive |
| Power (TDP) | • 75W |

* Using 50% weight sparsity

** @10 fps, H.264/H.265

NEXT STEPS

Order <u>Alveo V70 Development Card</u>

For additional questions, contact <u>V70_solutions@amd.com</u>

DISCLAIMERS

(The information contained herein is for informational purposes only and is subject to change without notice. While every precaution has been taken in the preparation of this document, it may contain technical inaccuracies, omissions and typographical errors, and AMD is under no obligation to update or otherwise correct this information. Advanced Micro Devices, Inc. makes no representations or warranties with respect to the accuracy or completeness of the contents of this document, and assumes no liability of any kind, including the implied warranties of noninfringement, merchantability or fitness for purposes, with respect to the operation or use of AMD hardware, software or other products described herein. No license, including implied or arising by estoppel, to any intellectual property rights is granted by this document. Terms and limitations applicable to the purchase or use of AMD's Standard Terms and Conditions of Sale.

COPYRIGHT NOTICE

© Copyright 2022 Advanced Micro Devices, Inc. All rights reserved. Xilinx, the Xilinx logo, AMD, the AMD Arrow logo, Alveo, Artix, Kintex, Kria, Spartan, Versal, Vitis, Virtex, Vivado, Zynq, and other designated brands included herein are trademarks of Advanced Micro Devices, Inc. Other product names used in this publication are for identification purposes only and may be trademarks of their respective companies. AMBA, AMBA Designer, ARM, ARM1176JZ-S, CoreSight, Cortex, and PrimeCell are trademarks of ARM in the EU and other countries. PCIe, and PCI Express are trademarks of PCI-SIG and used under license. PID1776694